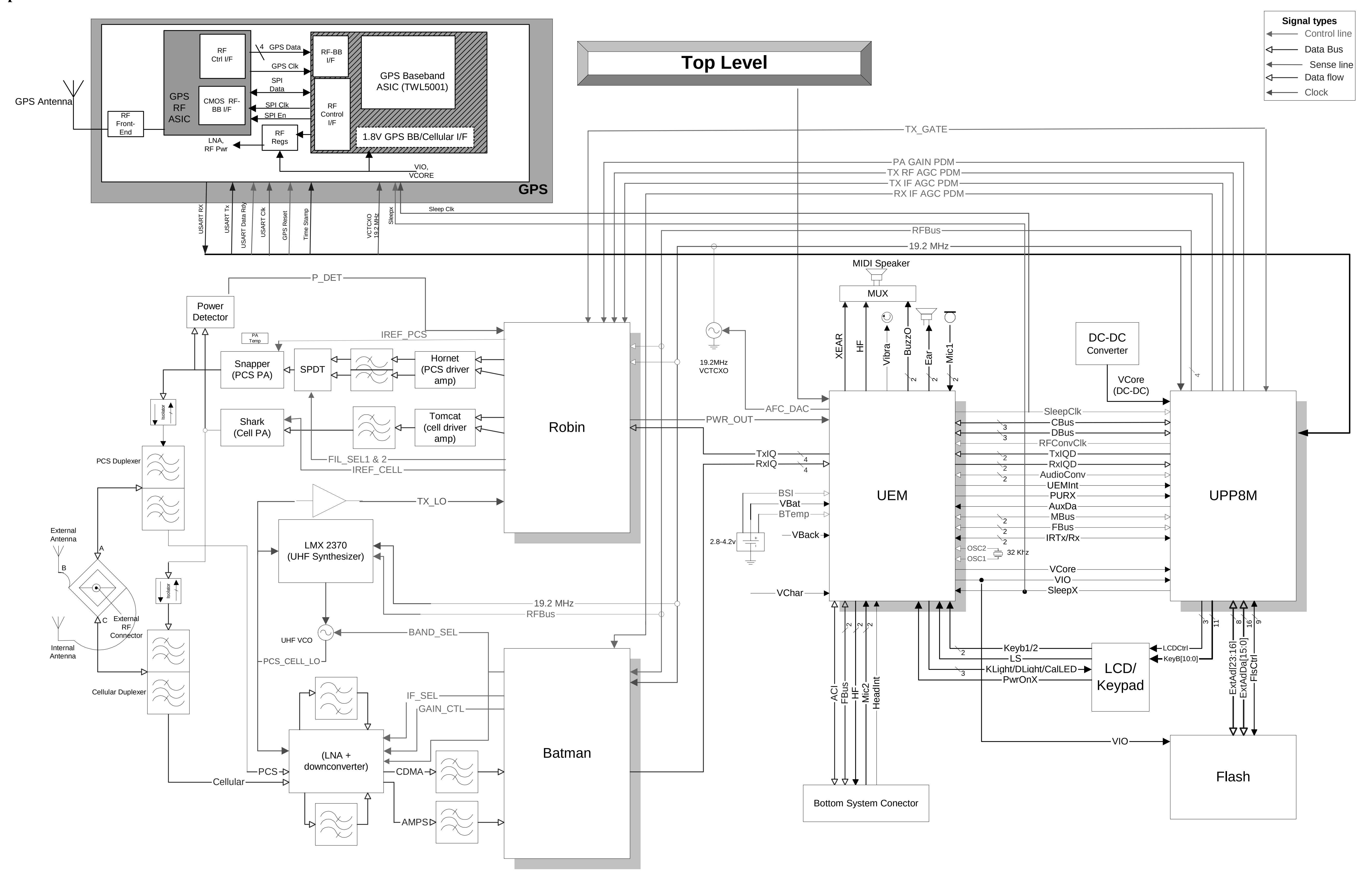
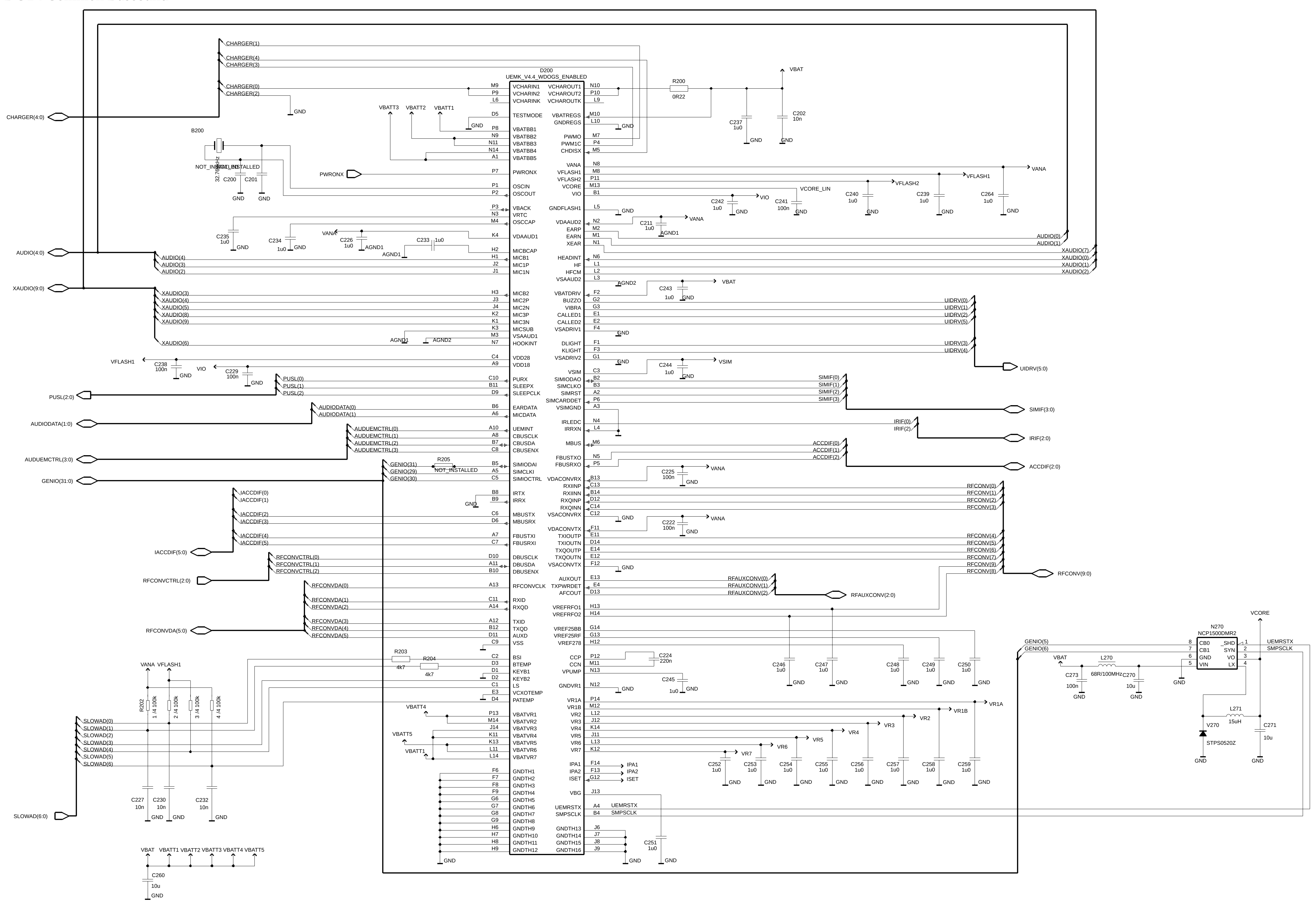
3585i Top Level Overview



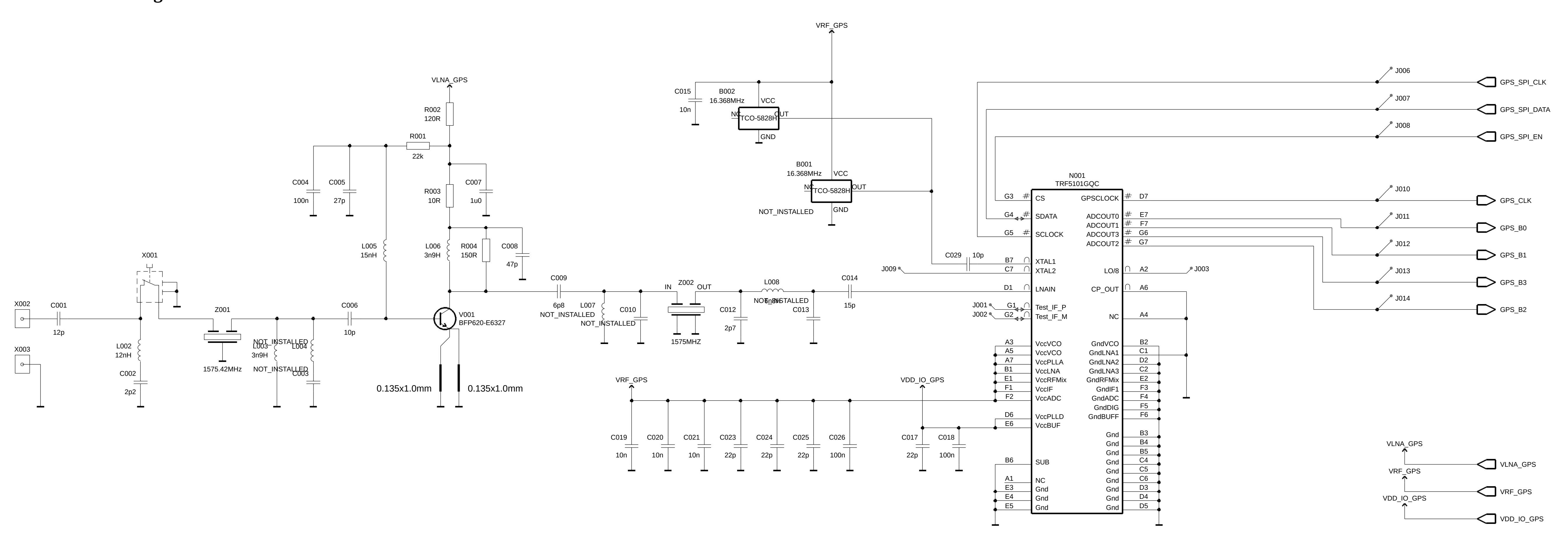
Audio

XAUDIO(9:0)
HOOKINT
HEADINT
MICB2
XAUDIO(3)
MIC2P
XAUDIO(4)
MIC2N
XAUDIO(5)
MIC3P
XAUDIO(8)
MIC3N
XAUDIO(9)
XEAR
XAUDIO(7)
HF
XAUDIO(1)
HFCM
XAUDIO(2)
L120 EXC24CB102U
L121 EXC24CB102U
C193 100n
C151 47p
C153 47p
C159 47p
C185 47p
AGND1
R150 1k0
R151 1k0
R164 1k0
R167 1k0
R152 47k
R166 47k
C154 100n
C155 100n
C165 100n
C168 100n
R159 1k5
C186 4u7
R169 47R
R160 22R
R165 22R
V152 EMIF02-MIC02F1
V153 EMIF02-MIC02F1
OUT1 IN1 OUT2 GND IN2
ESD_LEV=1 ESD_LEV=4
R162 1k0
R155 1k0
XMIC(1:0)
SLOWAD(6:0)
SLOWAD(2)
NOT_INSTALLED
X150
XEAR(1:0)
XEAR(1)
XEAR(0)
R153 14V/46V
R154 14V/46V
VIO
R195 47k
R179 100k
C169 100n
C198 1u0
N150 LM4890IBLX
BYPASS
_SHUTDOWN
IN+ IN- Vo1 Vo2
C2= VBAT
A2,B2= GND
VBAT
C196 1u0
C197 100p
C164 100n
R156 10k
SLOWAD(4)
L150 47R/100MHz NOT_INSTALLED
L151 47R/100MHz NOT_INSTALLED
B150
C157 220n
R158 10k
R185 100k
C172 47p
GENIO(12)
GENIO(28)
GENIO(31:0)
AUDIO(4:0)
C195 100n
L122 EXC24CB102U
C171 47p
C173 47p
R171 1k0
R173 1k0
R172 47k
C174 100n
C175 100n
R176 1k0
R178 1k0
MIC(1:0)
MIC(1)
MIC(0)
V154 IP4043CX5
C3 C1 A3 A1 B2
R180 22R
R181 22R
EAR(0:1)

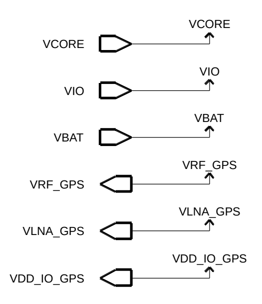
DCT4 Common Baseband



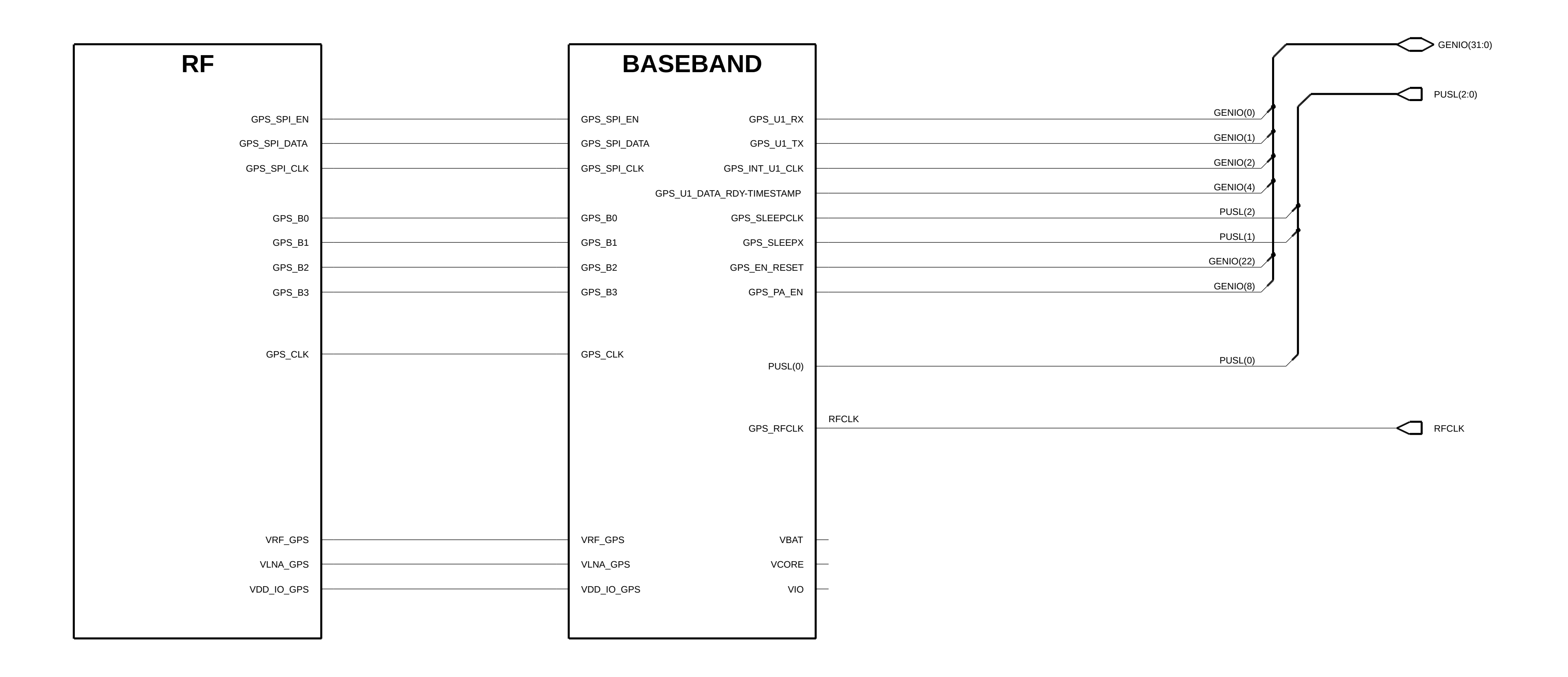
GPS RF Circuit Diagram



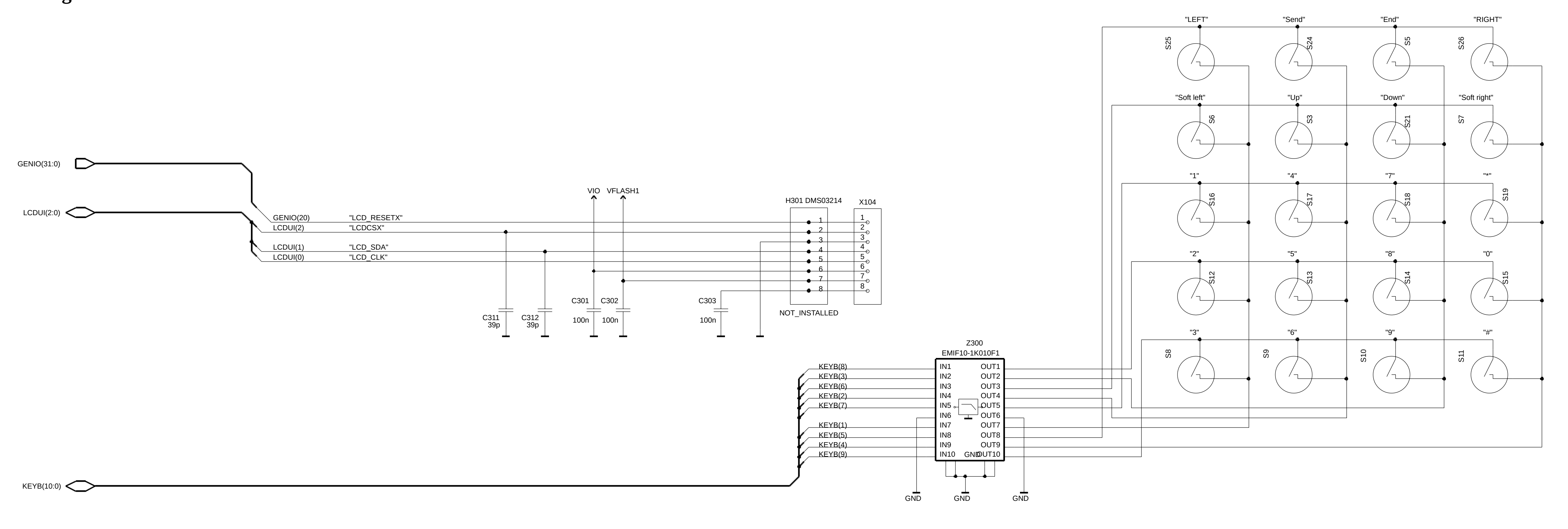
GPS Baseband Circuit Diagram

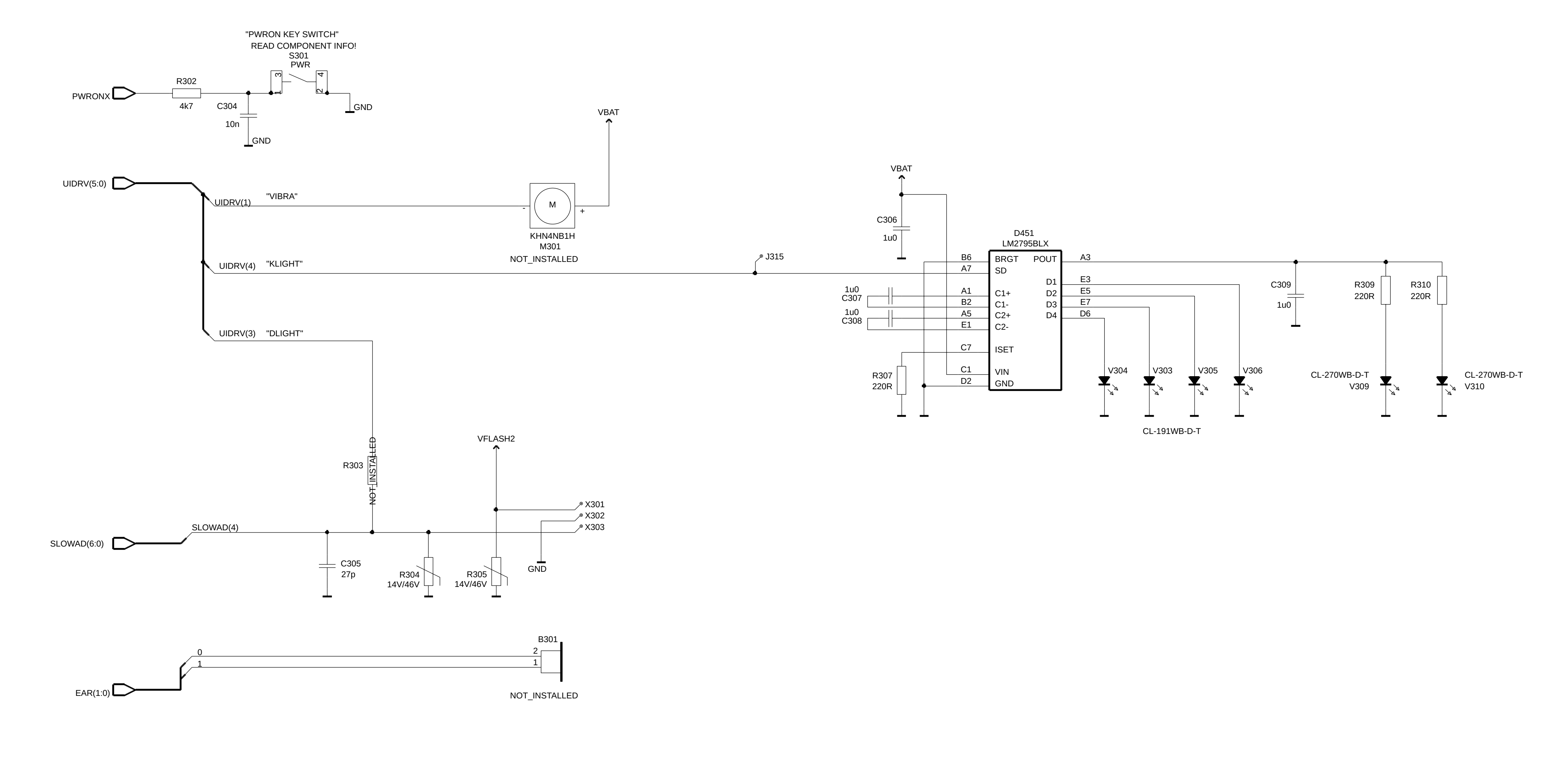


GPS RF-BB Interface

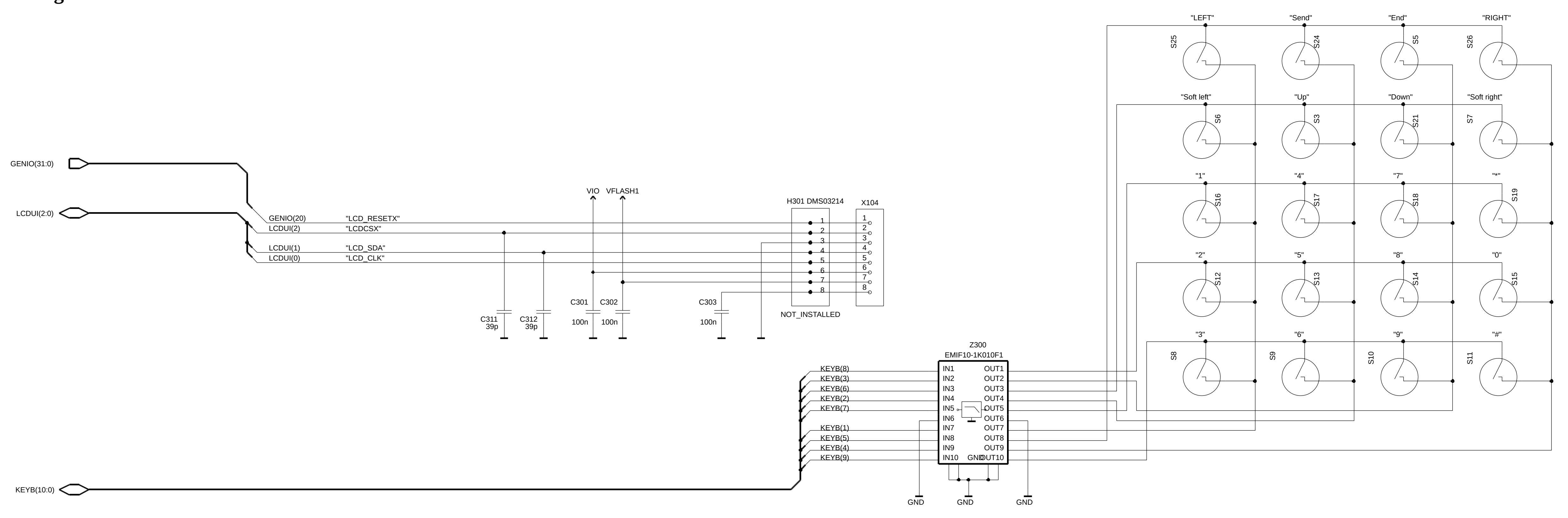


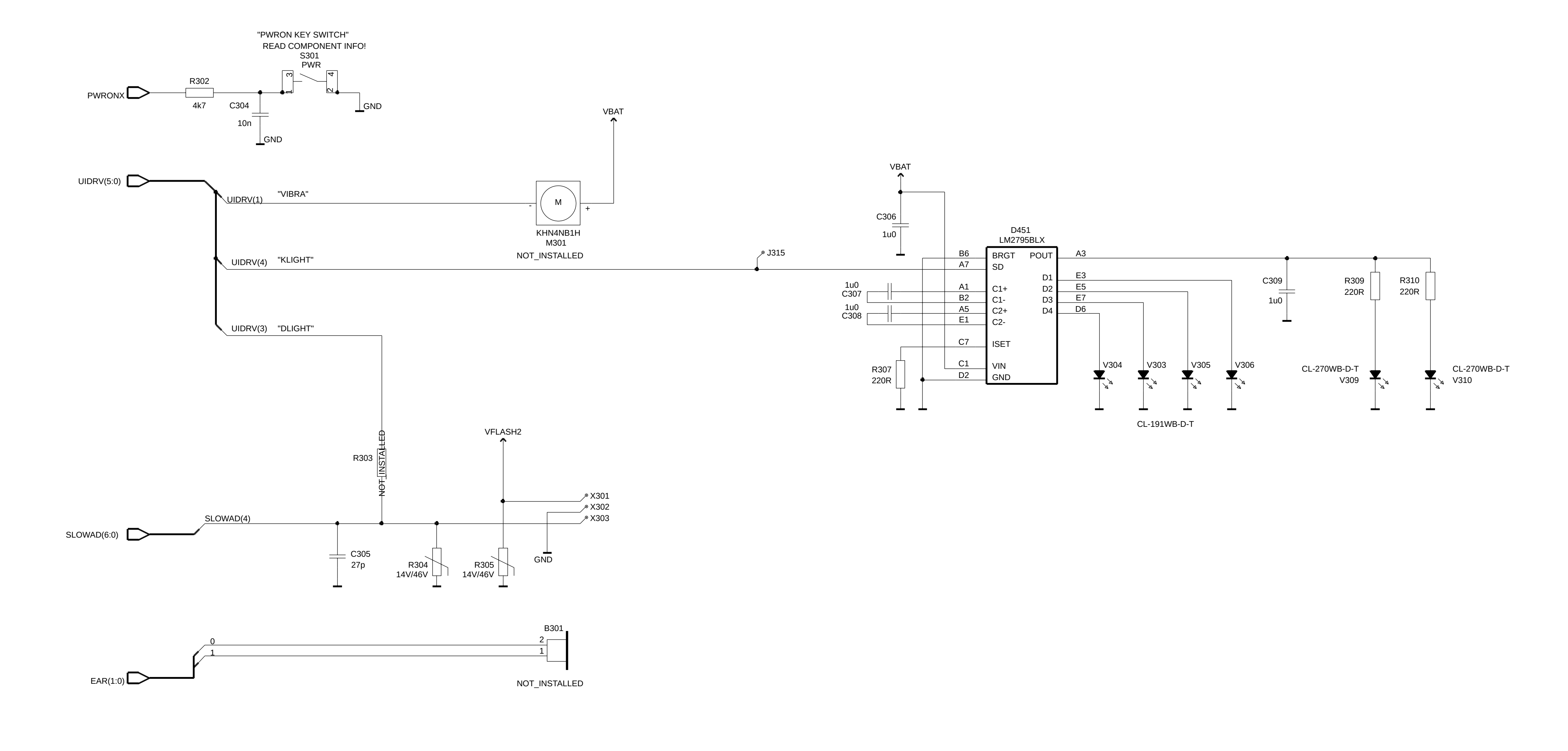
Circuit Diagram - UPP 8M



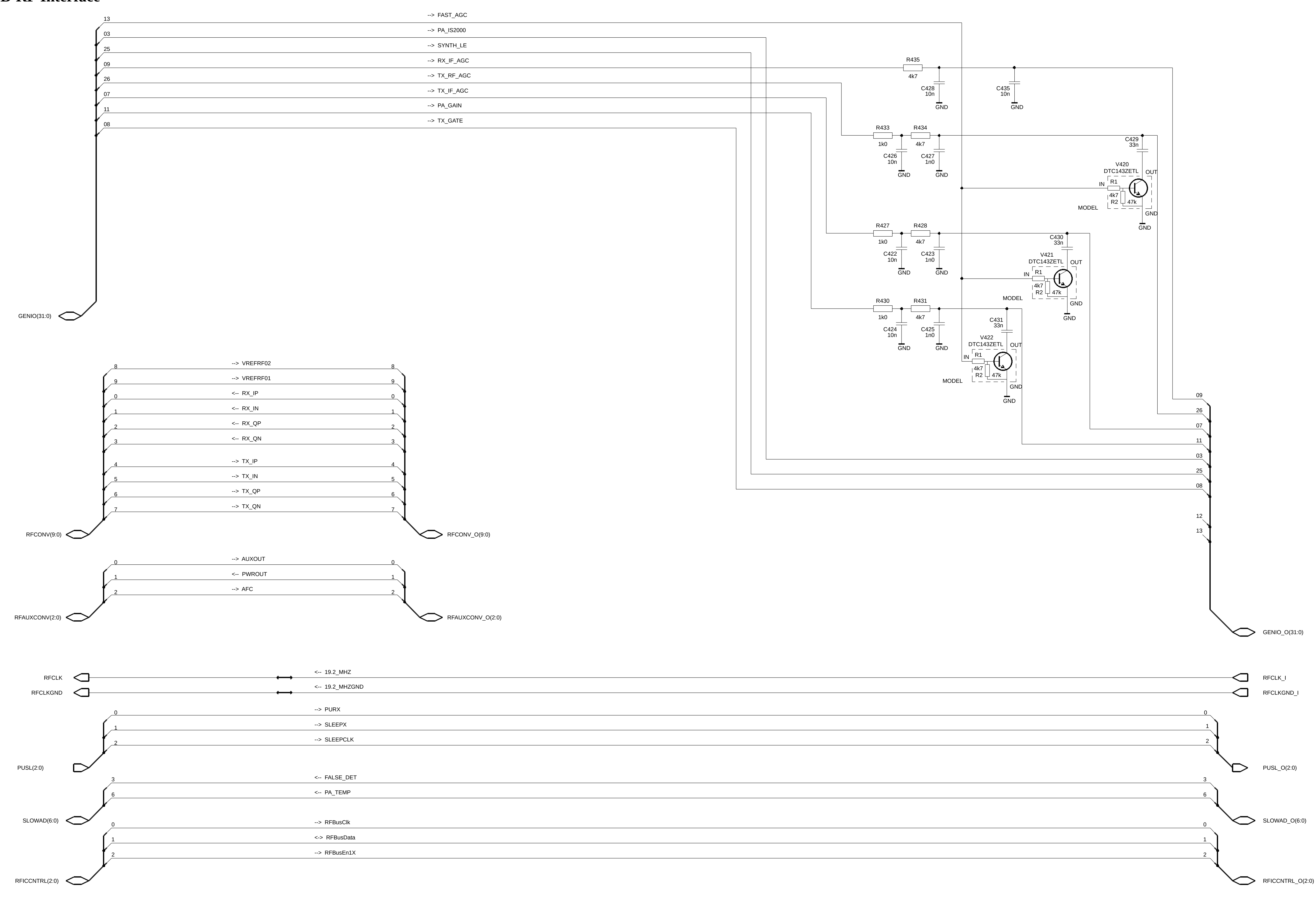


Circuit Diagram - User Interface





BB-RF Interface



System Connector

X102
1
2
3
4
NOT_INSTALLED
GND
VBATTRF
VBAT
C109 10p
C108 39p
C110 39p
GND
SLOWAD(6:0)
0 1 2 3 4 5 6

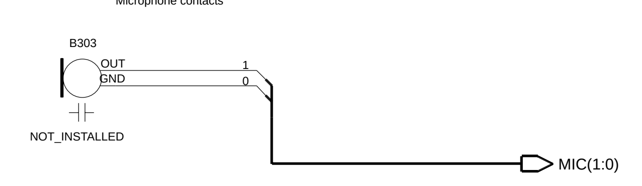
TOMAHAWK
BOTTOM CONNECTOR
Tomahawk
Charger connector
X103
+ 1
2
GND
NOT_INSTALLED
VCHAR
GND
F100 1.5A
L100 42R/100MHz
C106 1n0
C111 39p
V100 1PMT16AT3
GND
0 1 2
CHARGER(4:0)

VBAT
VIN N100
VOUT LP3985IBLX -2.8 VEN
BYPASS
GND
C101 1u0
C102 1u0
C107 10n
R110 100k
GND GND GND GND
24
GENIO(31:0)
HEADINT
VFLASH1
R812 100k

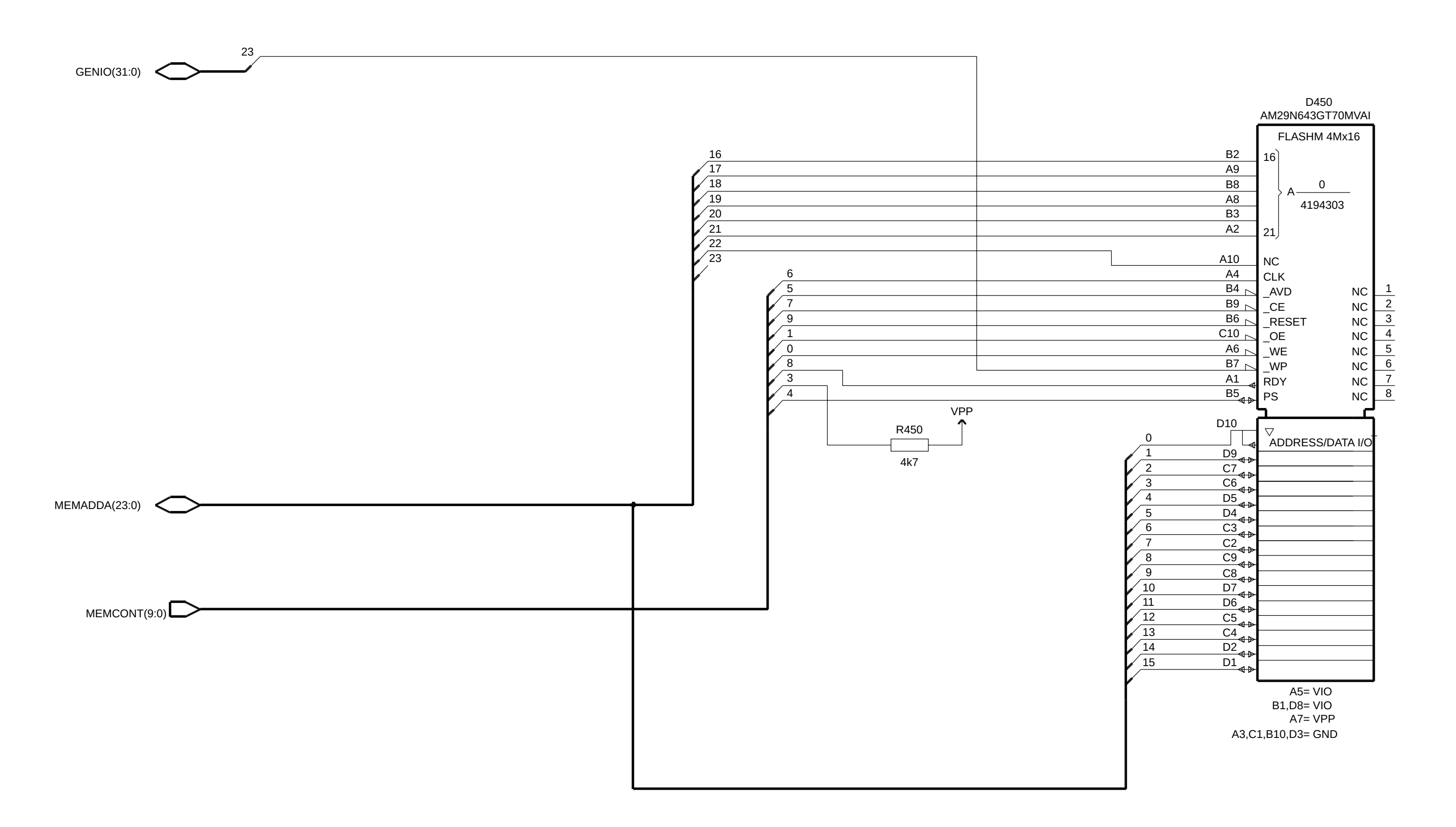
Tomahawk
System connector
X101
1 2 3 4 5 6 7 8 9 10 11 12 13 14
GND
AGND
GND
ACI
Vout
Fbus RX
Fbus TX
R102 2 /2 47R
R102 1 /2 47R
PRODTP2
PRODTP3
PRODTP7
R101 2 /2 47R
R101 1 /2 47R
R100 2 /2 47R
R100 1 /2 47R
0 2 1
C3 C1 A3 A1
IP4043CX5 V101
GND
B2
GND
C105 22p
C104 22p
R104 220k
C103 22p
GND GND GND GND
ACCDIF(2:0)

VSIM
X386
C707_10M006_086_2
6 5 4
1 2 3
NOT_INSTALLED
C389 1u0
C390 100n
GND GND
R386
EMIF03-SIM01
CLK
R1 R2 R3
GND
CLK
0 1 2 3
SIMIF(3:0)
GND

XMIC P
XMIC N
1 0
XMIC(1:0)
XEAR P
XEAR N
L103
EXC24CB102U
1 4
2 3
1 0
XEAR(1:0)



AMD 64 Mbit



VPP VIO VIO

C454 10n C450 10n C451 100n

GND GND GND

DCT4 Common Baseband - Top Level

AUDIO
HEADINT
MIC(1:0)
XMIC(1:0)
XEAR(1:0)
SLOWAD(6:0)
XAUDIO(9:0)
AUDIO(4:0)
EAR(1:0)
GENIO(31:0)

KEY_UI
EAR(1:0)
GENIO(31:0)
UIDRV(5:0)
PWRONX
KEYB(10:0)
LCDUI(2:0)
SLOWAD(6:0)

RF_BB
GENIO(31:0)
SLOWAD(6:0)
RFCONV(9:0)
RFAUXCONV(2:0)
RFCLK
PUSL(2:0)
RFCLKGND
RFICCNTRL(2:0)
GENIO_O(31:0)
SLOWAD_O(6:0)
RFCONV_O(9:0)
RFAUXCONV_O(2:0)
RFCLK_I
PUSL_O(2:0)
RFCLKGND_I
RFICCNTRL_O(2:0)

SYS_CONN
HEADINT
MIC(1:0)
XMIC(1:0)
XEAR(1:0)
GENIO(31:0)
SLOWAD(6:0)
ACCDIF(2:0)
CHARGER(4:0)
SIMIF(3:0)

POWER
AUDIO(4:0)
XAUDIO(9:0)
IRIF(2:0)
SLOWAD(6:0)
ACCDIF(2:0)
CHARGER(4:0)
SIMIF(3:0)
RFCONV(9:0)
RFAUXCONV(2:0)
UIDRV(5:0)
PWRONX
RFCONVDA(5:0)
RFCONVCTRL(2:0)
AUDIODATA(1:0)
AUDUEMCTRL(3:0)
IACCDIF(5:0)
PUSL(2:0)
GENIO(31:0)

UPP
LCDUI(2:0)
KEYB(10:0)
RFCLK
RFCONVDA(5:0)
RFCONVCTRL(2:0)
AUDIODATA(1:0)
AUDUEMCTRL(3:0)
IACCDIF(5:0)
PUSL(2:0)
GENIO(31:0)
RFCLKGND
RFICCNTRL(2:0)
JTAG_EMULATION(6:0)
DSP_MCUTEST(2:0)
MEMCONT(9:0)
MEMADDA(23:0)

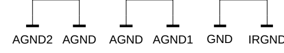
JTAG_EMULATION(3) TP28
JTAG_EMULATION(1) TP29
JTAG_EMULATION(2) TP30
JTAG_EMULATION(4) TP31
JTAG_EMULATION(0) VIO
JTAG_EMULATION(5) TP32
JTAG_EMULATION(6) TP33
U10
U11
TP36
GND

VBAT
X470
GND
STIRXD
STITXD
STISCLK
VBATT
NOT_INSTALLED
DSP_MCUTEST(2)
DSP_MCUTEST(0)
DSP_MCUTEST(1)
GND

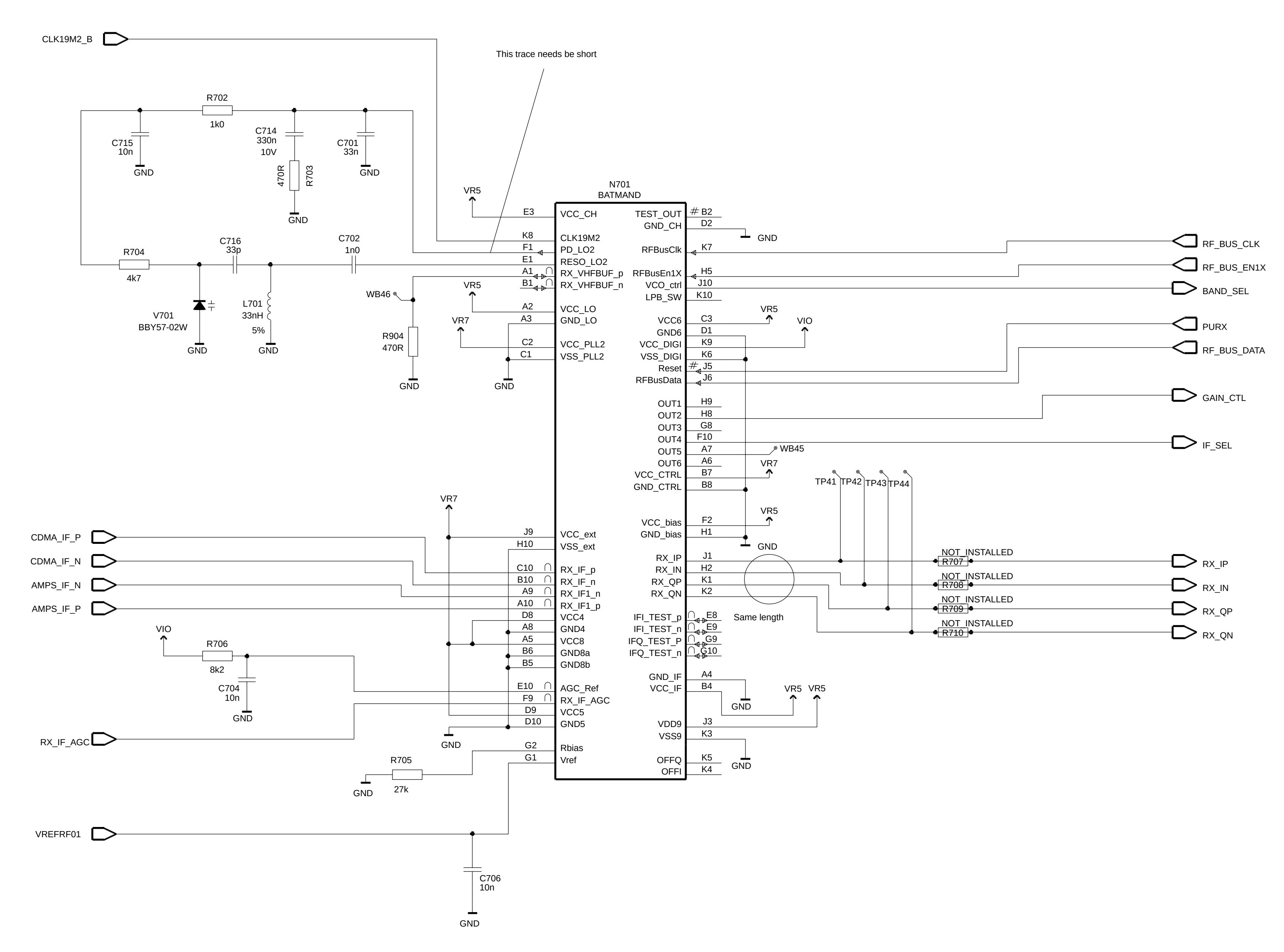
GPS_TOP
RFCLK
PUSL(2:0)
GENIO(31:0)

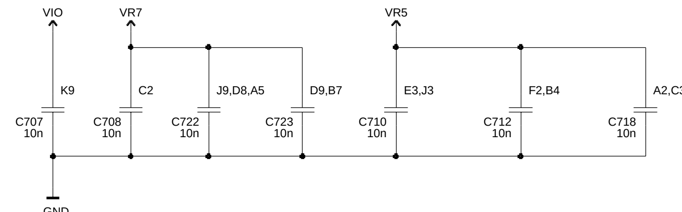
MEMORY
MEMADDA(23:0)
MEMCONT(9:0)
GENIO(31:0)

PRODTP7
PRODTP2
PRODTP3
VPP
J396
NOT_INSTALLED
GND

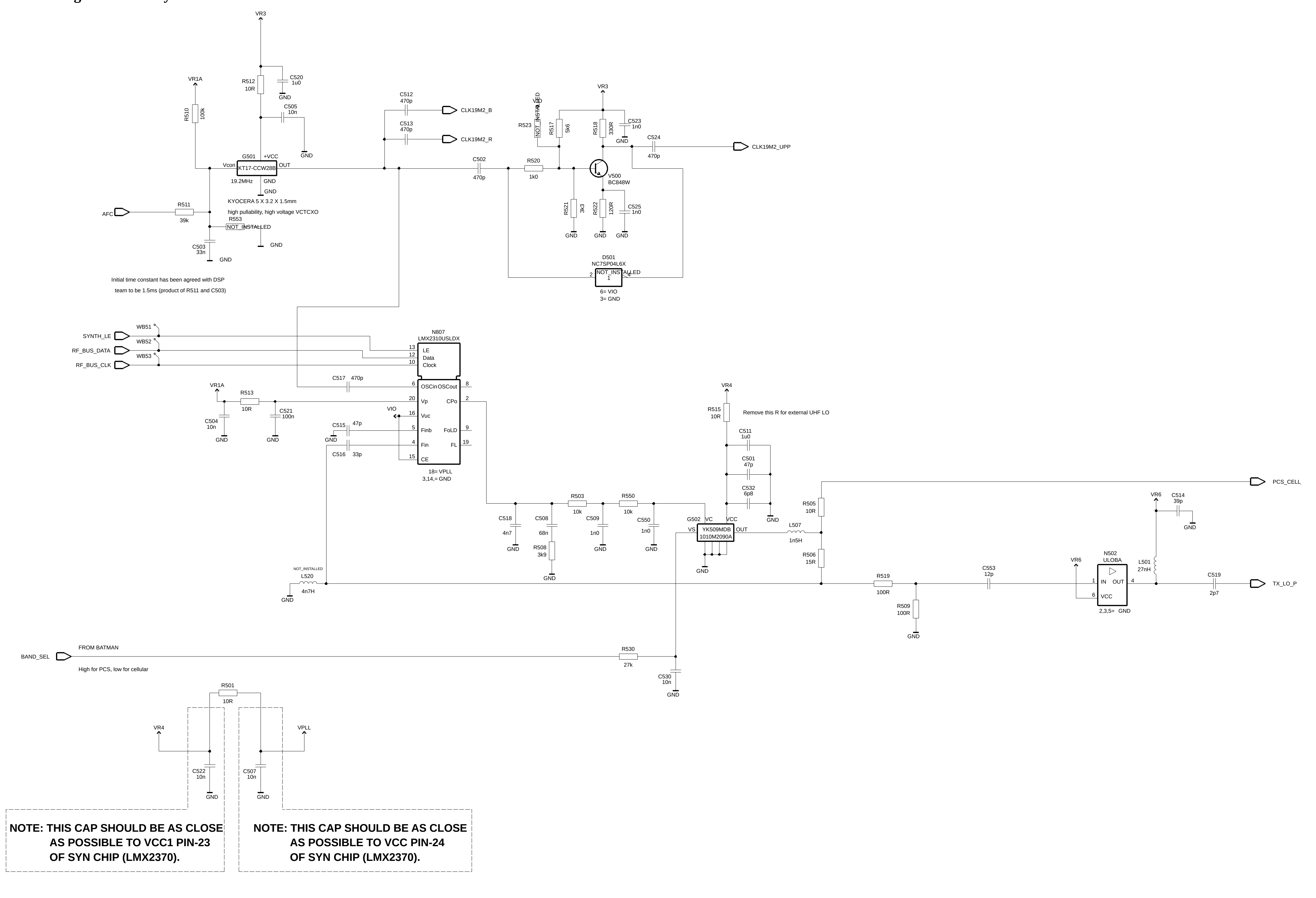


Circuit Diagram - Batman

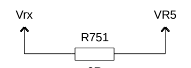




Circuit Diagram - UHF Synth

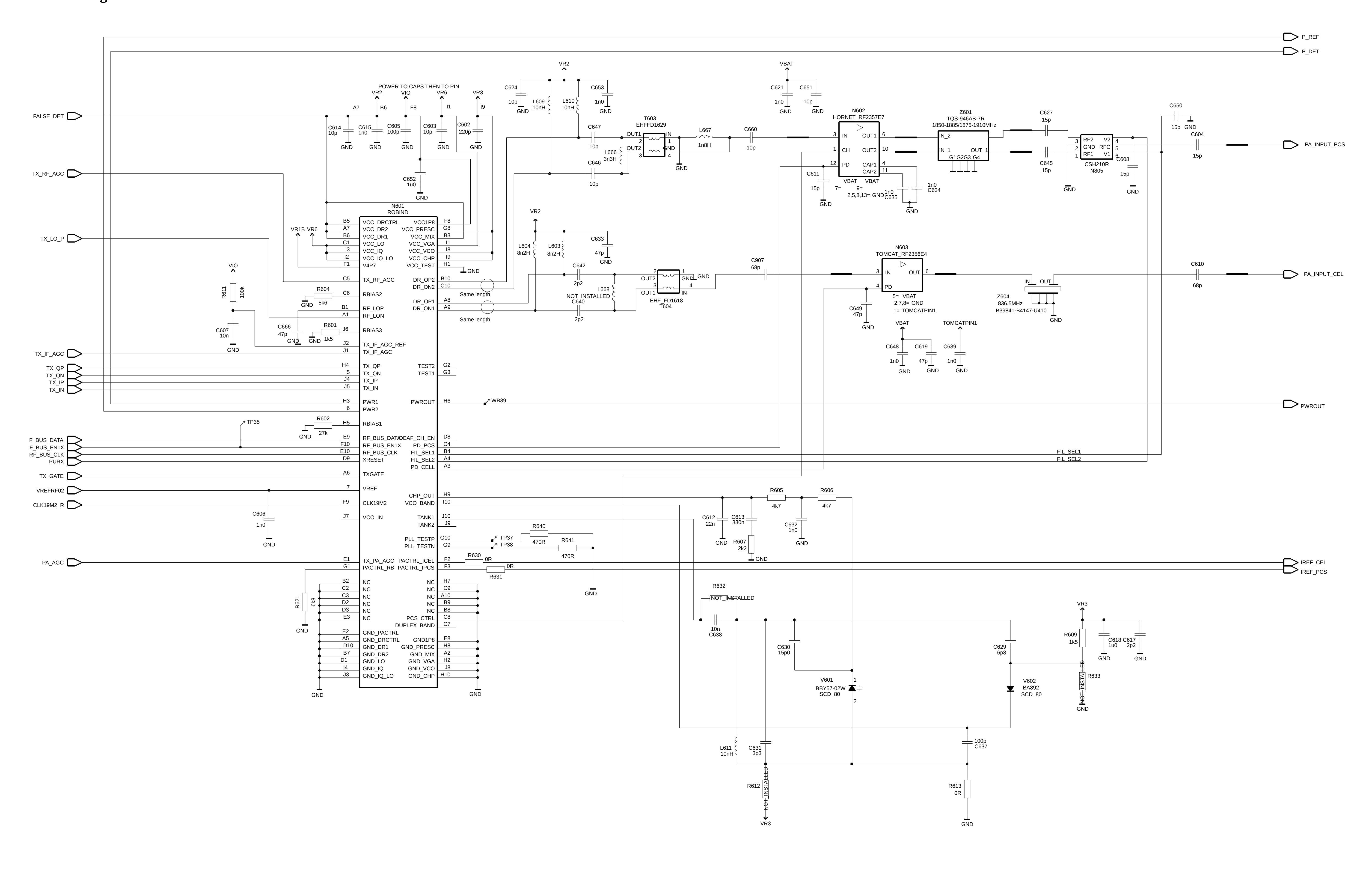


Circuit Diagram - Front End

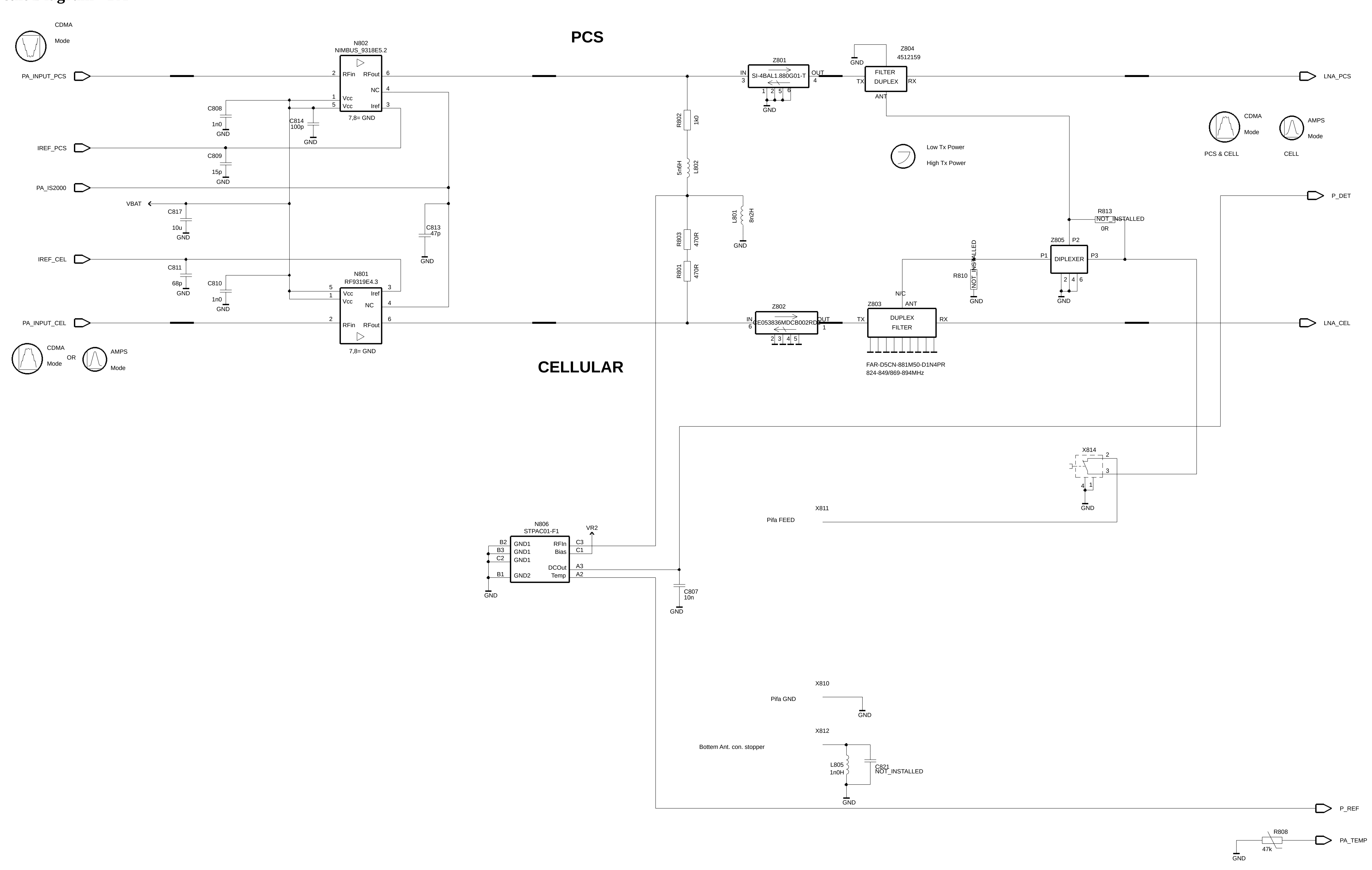


	LOGIC INPUTS		
MODES	BAND	GAIN_CTL	IF_SEL
CEL CDMA Hi Gain	0	1	0
CEL CDMA Lo Gain	0	0	0
PCS CDMA Hi Gain	1	1	0
PCS CDMA Lo Gain	1	0	0
AMPS Hi Gain	0	1	1
AMPS Lo Gain	0	0	1

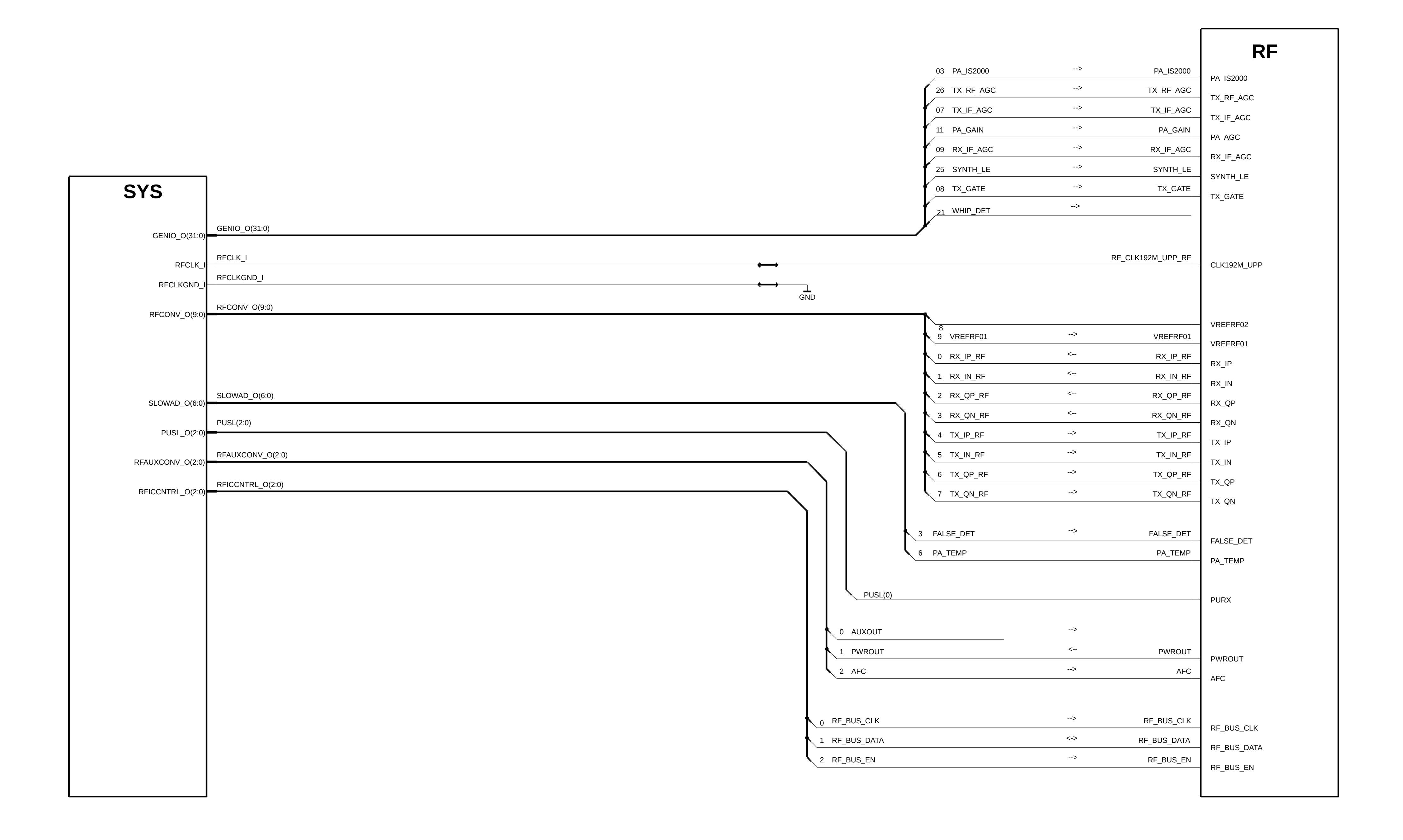
Circuit Diagram - Robin



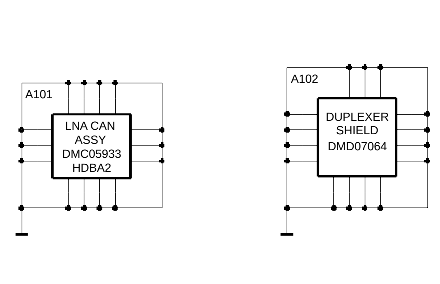
Circuit Diagram - PA

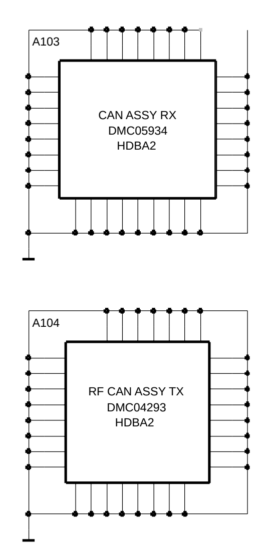


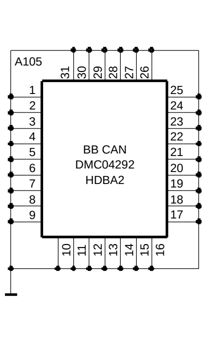
Circuit Diagram - Top Levell

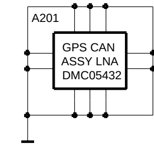


Shields

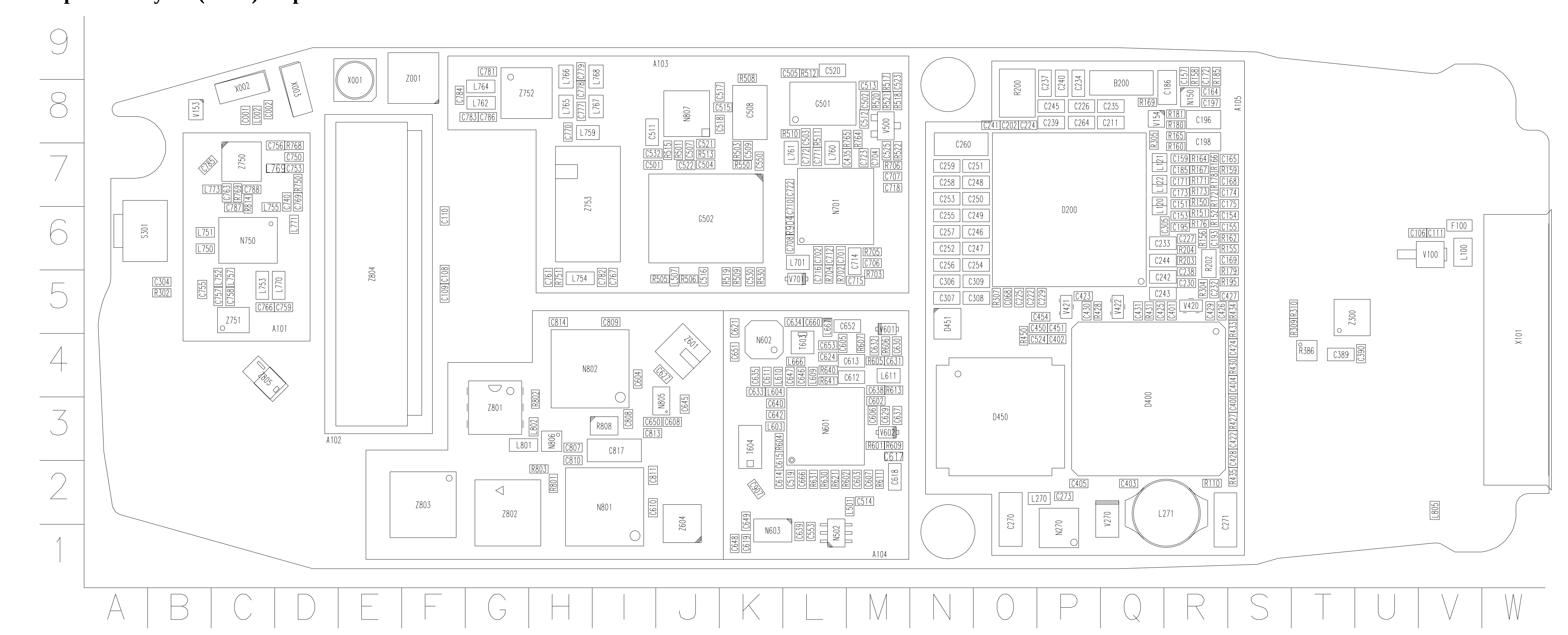

A101
LNA CAN
ASSY
DMC05933
HDBA2
A102
DUPLEXER
SHIELD
DMD07064


A103
CAN ASSY RX
DMC05934
HDBA2
A104
RF CAN ASSY TX
DMC04293
HDBA2


A105
BB CAN
DMC04292
HDBA2


A201
GPS CAN
ASSY LNA
DMC05432

Component Layout (3585i)- Top



Component Layout (3585i) - Bottom

